

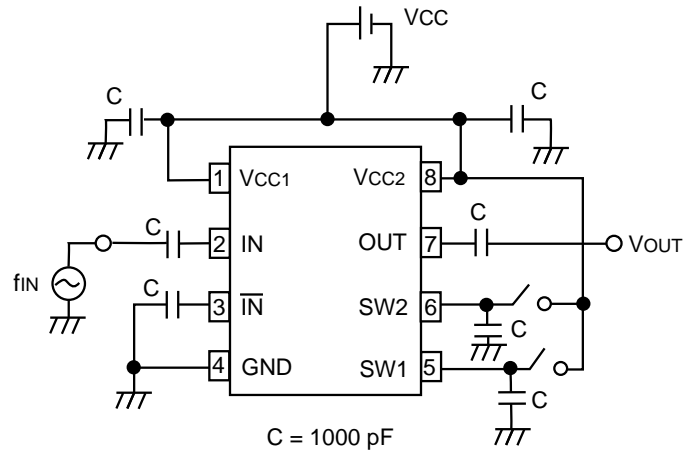
FEATURES

- HIGH FREQUENCY OPERATION TO 1 GHz
- SELECTABLE DIVIDE RATIO: ÷2, ÷4, ÷8
- WIDE SUPPLY VOLTAGE RANGE: 2.2 TO 5 V
- LOW SUPPLY CURRENT: 5.3 mA
- SMALL PACKAGE: 8 pin SSOP
- AVAILABLE IN TAPE AND REEL

DESCRIPTION

The UPB1509GV is a Silicon MMIC digital prescaler manufactured with the NESAT™ IV silicon bipolar process. It features frequency response to 1 GHz, selectable divide-by-two, four, or eight modes, and operates from a 3 to 5 volt supply while drawing only 5.3 milliamps. The device is housed in a small 8 pin SSOP package that contributes to system miniaturization. The low power consumption and wide supply range makes the device well suited for cellular and cordless telephones as well as DBS receiver applications.

TEST CIRCUIT



ELECTRICAL CHARACTERISTICS (TA = -40 to +85°C, Vcc = 2.2 to 5.5 V, unless otherwise noted)

PART NUMBER PACKAGE OUTLINE			UPB1509GV S08		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Supply Current, No Input Signal, Vcc = 3 V	mA	2.5	5.0	5.9
f _{IN} (u)	Upper Limit Operating Frequency, P _{IN} = -20 to 0 dBm P _{IN} = -20 to -5 dBm @ ÷2 @ ÷4 @ ÷8	MHz MHz MHz MHz	500 700 800 1000		
f _{IN} (L)	Lower Limit Operating Frequency, P _{IN} = -20 to 0 dBm P _{IN} = -20 to -5 dBm	MHz MHz			50 500
P _{IN}	Input Power, f _{IN} = 50 to 1000 MHz f _{IN} = 50 to 500 MHz	dBm dBm	-20 -20		-5 0
V _{OUT}	Output Voltage, R _L = 200 Ω	V _{P-P}	0.1	0.2	
V _{IN(H)}	Division Ratio Control Voltage High	V		V _{cc}	
V _{IN(L)}	Division Ratio Control Voltage Low	V		OPEN	

ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V _{CC1} , V _{CC2}	Supply Voltage	V	6.0
V _{IN}	Input Voltage	V	6.0
P _D	Power Dissipation ²	mW	250
T _{OP}	Operating Temperature	°C	-45 to +85
T _{STG}	Storage Temperature	°C	-55 to +150

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.
2. Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB (T_A = +85°C).

RECOMMENDED OPERATING CONDITIONS

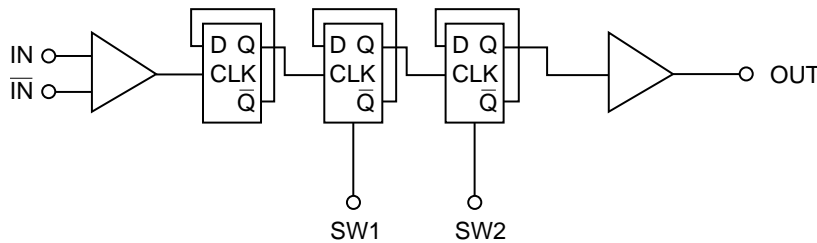
SYMBOL	PARAMETER	UNITS	MIN	TYP	MAX
V _{CC1} , V _{CC2}	Supply Voltage	V	2.2	3.0	5.5
T _{OP}	Operating Temperature	°C	-40	+25	+85

PRODUCT LINE-UP

Product No.	I _{CC} (mA)	V _{CC} (V)	+2 f _{IN} (MHz)	+4 f _{IN} (MHz)	+8 f _{IN} (MHz)	Package
UPB587G	5.5	2.2 to 3.5	50 to 300	50 to 600	50 to 1000	8 pin SOP
UPB1509GV	5.3	2.2 to 5.5	50 to 1000	50 to 1000	50 to 1000	8 pin SSOP

Note: This table shows Typ. values only.

INTERNAL BLOCK DIAGRAM

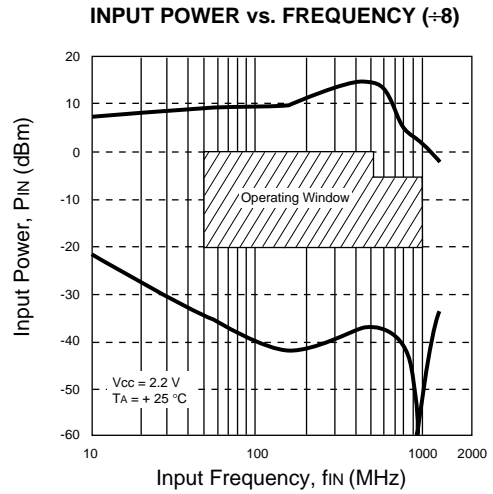
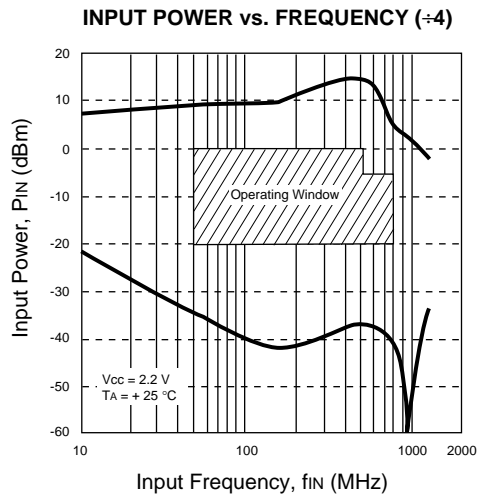
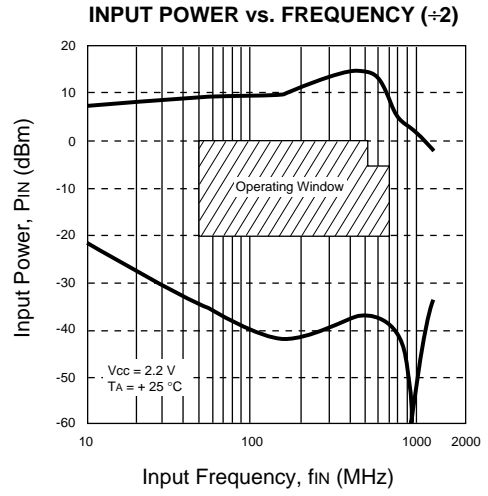
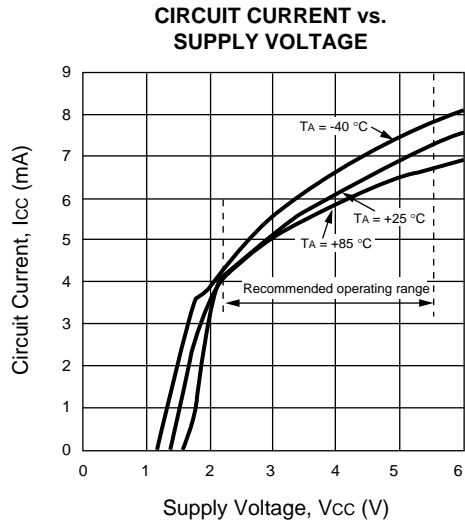


PIN DESCRIPTIONS

Pin no.	Symbol	Applied Voltage	Pin Voltage	Description													
1	VCC1	2.2 to 5.5	–	Power supply pin of input amplifier and dividers. This pin must be equipped with bypass capacitor (eg 1000 pF) to ground.													
2	IN	–	1.7 to 4.95	Signal input pin. This pin should be coupled with a capacitor (eg 1000 pF).													
3	IN-bar	–	1.7 to 4.95	Signal input bypass pin. This pin must be equipped with a bypass capacitor (eg 1000 pF) to ground.													
4	GND	0	–	Ground pin. Ground pattern on the board should be formed as wide as possible to minimize ground impedance.													
5	SW1	H/L (VCC/OPEN)	–	Divided ratio control pin. Divide ratio can be controlled by the following input voltages to these pins. <table border="1" style="margin: 10px auto;"> <thead> <tr> <th colspan="2" rowspan="2"></th> <th colspan="2">SW2</th> </tr> <tr> <th>H (Vcc)</th> <th>L (OPEN)</th> </tr> </thead> <tbody> <tr> <th rowspan="2">SW1</th> <th>H (Vcc)</th> <td>1/2</td> <td>1/4</td> </tr> <tr> <th>L (OPEN)</th> <td>1/4</td> <td>1/8</td> </tr> </tbody> </table>			SW2		H (Vcc)	L (OPEN)	SW1	H (Vcc)	1/2	1/4	L (OPEN)	1/4	1/8
		SW2															
		H (Vcc)	L (OPEN)														
SW1	H (Vcc)	1/2	1/4														
	L (OPEN)	1/4	1/8														
6	SW2	H/L (VCC/OPEN)	–	These pins must each be equipped with a bypass capacitor to ground.													
7	OUT	–	1.0 to 4.7	Divided frequency output pin. This pin is designed as an emitter follower output. This pin can output 0.1 V _{p-p} min with a 200 Ω load. This pin should be coupled to load device with a capacitor (eg 1000 pF).													
8	VCC2	2.2 to 5.5	–	Power supply pin of output buffer amplifier. This pin must be equipped with bypass capacitor (eg 1000 pF) to ground.													

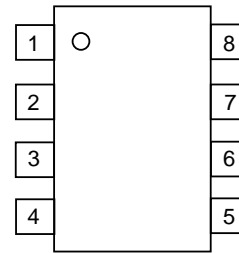
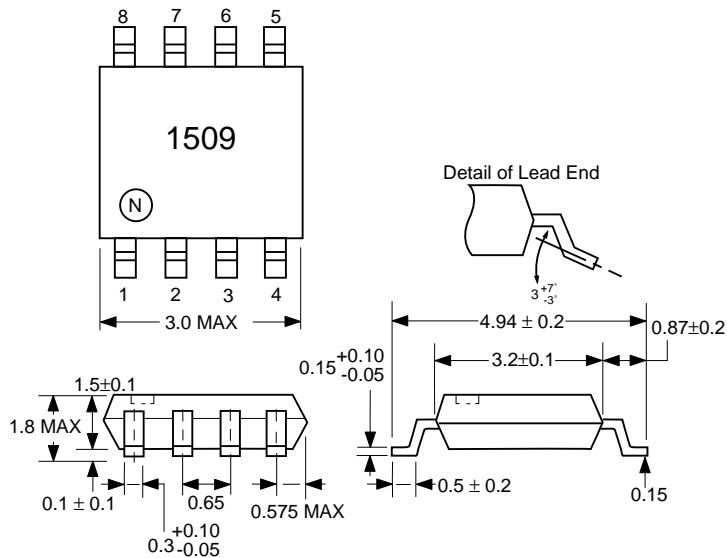
TYPICAL PERFORMANCE CURVES

(TA = +25°C unless otherwise noted)



OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S08



PIN CONNECTIONS

- 1. Vcc1
- 2. IN
- 3. $\overline{\text{IN}}$
- 4. GND
- 5. SW1
- 6. SW2
- 7. OUT
- 8. Vcc2

ORDERING INFORMATION

PART NUMBER	QUANTITY
UPB1509GV-E1	1000/Reel

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